

AMENDMENTS TO THE CLAIMS

Please amend the claims as follows:

1. (Currently Amended) An information handling system comprising:
  - a processor;
  - a memory coupled to the processor;
  - an interrupt controller coupled to the processor;
  - a dynamic router implemented as a programmable gate array, coupled to the interrupt controller, for allocating interrupts to a plurality of devices for providing unshared interrupts to the devices;
    - a first device which requests a first interrupt and which is assigned the first interrupt by the dynamic router; and
    - a second device which requests the first interrupt and which is instead assigned a second interrupt by the dynamic router such that interrupt requests are dynamically allocated to provide a more balanced interrupt distribution, whereby interrupt sharing and corresponding delays are reduced.
2. (Original) The information handling system of claim 1 further comprising an interrupt table for storing information relative to which interrupt is associated with which device.
3. (Original) The information handling system of claim 2 further comprising an operating system and a data path between the operating system and the interrupt table such that the operating system is informed of the dynamically allocated interrupts by the interrupt table.

4. (Original) The information handling system of claim 1 further comprising a third device which generates a higher number of interrupt requests than the first device, the first and third devices sharing the first interrupt.
5. (Original) The information handling system of claim 1 further comprising a third device which generates a higher number of interrupt requests than the second device, the second and third devices sharing the second interrupt.
6. (Original) The information handling system of claim 1 further comprising a third device which generates a lower number of interrupt requests than the first device, the first and third devices sharing the first interrupt.
7. (Original) The information handling system of claim 1 further comprising a third device which generates a lower number of interrupt requests than the second device, the second and third devices sharing the second interrupt.
8. (Currently Amended) An information handling system comprising:
  - a processor situated on a circuitry board;
  - a plurality of expansion slots mounted on the circuitry board for receiving devices;
  - a memory, coupled to the processor, to facilitate execution of programs by the processor;
  - an interrupt controller coupled to the processor;
  - a dynamic router implemented as a programmable gate array, coupled to the interrupt controller, for allocating interrupts to a plurality of devices for providing unshared interrupts to the devices;
  - a first device, situated in one of the expansion slots, which requests a first interrupt and which is assigned the first interrupt by the dynamic router; and

a second device, situated in another of the expansion slots, which requests the first interrupt and which is instead assigned a second interrupt by the dynamic router such that interrupt requests are dynamically allocated to provide a more balanced interrupt distribution, whereby interrupt sharing and corresponding delays are reduced.

9. (Original) The information handling system of claim 8 further comprising an interrupt table for storing information relative to which interrupt is associated with which device.
10. (Original) The information handling system of claim 9 further comprising an operating system and a data path between the operating system and the interrupt table such that the operating system is informed of the dynamically allocated interrupts by the interrupt table.
11. (Original) The information handling system of claim 8 further comprising a third device which generates a higher number of interrupt requests than the first device, the first and third devices sharing the first interrupt.
12. (Original) The information handling system of claim 8 further comprising a third device which generates a higher number of interrupt requests than the second device, the second and third devices sharing the second interrupt.
13. (Original) The information handling system of claim 8 further comprising a third device which generates a lower number of interrupt requests than the first device, the first and third devices sharing the first interrupt.

14. (Original) The information handling system of claim 8 further comprising a third device which generates a lower number of interrupt requests than the second device, the second and third devices sharing the second interrupt.
15. (Currently Amended) A method of allocating interrupts in an information handling system comprising:
  - providing an interrupt controller;
  - implementing a dynamic router as a programmable gate array, coupled to the interrupt controller, for allocating interrupts to a plurality of devices for providing unshared interrupts to the devices;
  - assigning a first interrupt to a first device when the first device requests to be assigned the first interrupt; and
  - assigning a second interrupt to a second device when the second device requests to be assigned the first interrupt, whereby interrupt sharing and corresponding delays are reduced.
16. (Original) The method of claim 15 further comprising informing an operating system in the information handling system that the second interrupt is assigned to the second device.
17. (Original) The method of claim 15 further comprising determining if the first device generates a large number of interrupt requests and if so sharing the first interrupt with a third device which generates a lower number of interrupt requests than the first device.
18. (Original) The method of claim 15 further comprising determining if the second device generates a large number of interrupt requests and if so sharing the

second interrupt with a third device which generates a lower number of interrupt requests than the first device.

19. (Original) The method of claim 15 further comprising determining if the first device generates a low number of interrupt requests and if so sharing the first interrupt with a third device which generates a high number of interrupt requests.
20. (Original) The method of claim 15 further comprising determining if the second device generates a low number of interrupt requests and if so sharing the second interrupt with a third device which generates a high number of interrupt requests.
21. (Original) The method of claim 15 wherein the first and second devices populate respective first and second expansion slots in the system.
22. (Original) The method of claim 15 wherein the first and second devices are on-board devices.
23. (Original) The method of claim 15 wherein one of the first and second devices populates an expansion slot in the system and the other of the first and second devices is an on-board device.